

ABSTRACT OF THE DISCLOSURE

A multi-layer electronic structure includes an increased capacity for the attachment of active or passive devices thereto. This is achieved by creating a three-dimensional grid of connection points to electrically couple active or passive surface mounted devices to edge mounted devices. The grid pattern is useful with any laminate including circuit cards, ceramic modules and flexible circuits. The variety of electrical devices that may be connected to the cross-sectional substrate includes, but is not limited to, chips such as semiconductor chips, diodes, resistors, capacitors and printed wiring boards. The structure can be used to more rapidly pass data, such as optical data that is transmitted from a spectroscope through a VCSEL laser and the electronic structure to a computer for diagnostics and analysis. A stepped arrangement of circuitized laminates is described for this purpose.

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